



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2675
3/A
4/16/02

Inventor Applicant: Samson Huang § Art Unit: 2675
Serial No.: 09/493,319 ✓ §
Filed: January 28, 2000 § Examiner: Leland R. Jorgensen
Title: Optical Display Device § Docket No. ITL.0312US
(P7995)

Commissioner for Patents
Washington, DC 20231

RECEIVED

APR 16 2002

REPLY TO OFFICE ACTION DATED FEBRUARY 27, 2002

Technology Center 2600

Dear Sir:

Please amend the application as follows.

In the Abstract:

Replace the Abstract with the following:

--A method includes providing a capacitor to maintain a terminal voltage of a pixel cell near a predetermined voltage. A memory is provided to store a digital indication of the predetermined voltage, and during a refresh operation, the digital indication is converted into an analog voltage to update a charge on the capacitor. A light modulator cell includes a pixel cell, a capacitor, a memory and a digital-to-analog converter. The capacitor maintains a terminal voltage of the pixel cell near a predetermined voltage, and the memory stores a digital indication of the predetermined voltage. The digital-to-analog converter converts the digital indication into an analog voltage to update a charge on the capacitor during a refresh operation.--

In the Specification:

Replace the paragraph beginning on line 6 of page 3 with the following:

Because the array 6 might be quite large, the number of signal lines 12 typically is considerably smaller than the number of column lines 16. Therefore, the signal lines 12 typically are used to sequentially access the SLM cells 20 K cells at a time (where "K" represents the number of signal lines and typically is less than the number (M) of columns) by activating the appropriate transistors 18. Because only K bit lines 16 are driven with new values (and thus,

Date of Deposit: 3-22-02
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, Washington, DC 20231.
Debra Cutrona